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<b>TRANSMITTAL FORM</b>  (to be used for all correspondence after initial filing)	Application Number	10/006,810	
	Filing Date	12/03/2001	
	First Named Inventor	Jayson Trtnh	
	Art Unit	2637	
	Examiner Name	Qurbuddin Ghulamali	
Total Number of Pages in This Submission	13	Attorney Docket Number	01-S3

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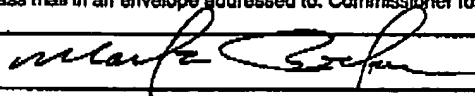
OCT 25 2005

ENCLOSURES (Check all that apply)		
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## SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	Lattice Semiconductor Corporation		
Signature			
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Date	10/25/05	Reg. No.	31325

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Named Inventor: Jayson Trinh  
Application No.: 10/006,610  
Filed: 12/03/2001  
For: CLOCK DATA RECOVERY DESERIALIZER WITH  
PROGRAMMABLE SYNC DETECT LOGIC  
Examiner: Qutbuddin Ghulamali  
Art Unit: 2637  
Attorney Docket No. 01-53

**RESPONSE AFTER FINAL OFFICE ACTION**

Applicants respond to the final Office action mailed 8/30/05 as follows.